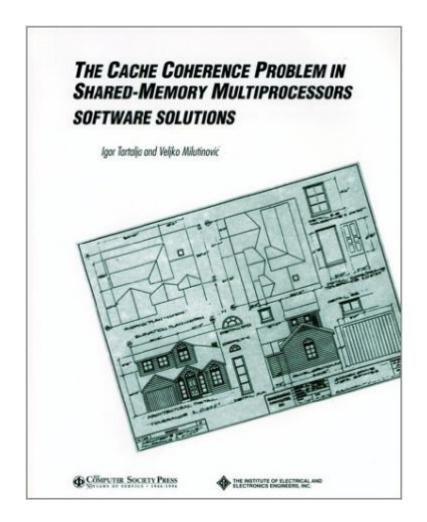
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## The Cache Coherence Problem In Shared-Memory Multiprocessors: Software Solutions (Systems)





## Synopsis

Almost all software solutions are developed through academic research and implemented only in prototype machines leaving the field of software techniques for maintaining the cache coherence widely open for future research and development. This book is a collection of all the representative approaches to software coherence maintenance including a number of related efforts in the performance evaluation field. The book presents a selection of 27 papers dealing with state-of-the-art software solutions for cache coherence maintenance in shared-memory multiprocessors. It begins with a set of four introductory readings that provides a brief overview of the cache coherence problem and introduces software solutions to the problem. The text defines and illustrates static and dynamic software schemes, techniques for modeling performance evaluation mechanisms, and performance evaluation studies. The book is intended for the experienced reader in computer engineering but possibly a novice in the topic of cache coherence. It also provides an in-depth understanding of the problem as well as a comprehensive overview for multicomputer designers, computer architects, and compiler writers. In addition, it is a software coherence reference handbook for advanced undergraduate and typical graduate students in multiprocessing and multiprogramming areas.

## **Book Information**

Series: Systems (Book 10) Paperback: 358 pages Publisher: Wiley-IEEE Computer Society Pr; 1 edition (January 30, 1996) Language: English ISBN-10: 0818670967 ISBN-13: 978-0818670961 Product Dimensions: 8.2 x 0.8 x 11 inches Shipping Weight: 1.8 pounds (View shipping rates and policies) Average Customer Review: Be the first to review this item Best Sellers Rank: #5,956,317 in Books (See Top 100 in Books) #85 in Books > Computers & Technology > Programming > Algorithms > Memory Management #2767 in Books > Computers & Technology > Hardware & DIY > Microprocessors & System Design #5263 in Books > Textbooks > Computer Science > Networking

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